

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	REDUCED DIELECTRIC CONSTANT SPACER MATERIALS INTEGRATION FOR HIGH SPEED LOGIC GATES							
Application Number :								
Date :								
First Named Applicant:	Michael P. Belyansky							
Confirmation Number:								
Attorney Docket Number:	FIS920040047US1							
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>H. Daniel Schnurmann Registered Number: 35791</td><td>/H. Daniel Schnurmann Reg. No. 35,791/</td><td>Agent</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	H. Daniel Schnurmann Registered Number: 35791	/H. Daniel Schnurmann Reg. No. 35,791/	Agent
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Documents being submitted:		Files
us-ids		FIS920040047US1-usidst.xml us-ids.dtd us-ids.xsl
us-request		FIS920040047US1-usrequ.xml us-request.dtd us-request.xsl
us-assignment		FIS920040047US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS920040047US1Assignment1.tif FIS920040047US1Assignment2.tif
us-fee-sheet		FIS920040047US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body		FIS920040047US1-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd
us-declaration		FIS920040047US1Drawing1.tif
us-declaration		FIS920040047US1Declaration3.tif
us-declaration		FIS920040047US1Declaration4.tif
us-declaration		FIS920040047US1Declaration5.tif
us-declaration		FIS920040047US1Declaration1.tif
us-declaration		FIS920040047US1Declaration2.tif
Comments		

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